

SPECIFICATION

5 Docket No. 272PQ

BE IT KNOWN that I, Owen Jones, have invented new and useful improvements

NOISE CANCELLATION SYSTEM FOR ACTIVE HEADSETS

of which the following is a specification:

1

BACKGROUND OF THE INVENTION

2

3

1. Field of the Invention:

4

5 This invention relates generally to a noise cancellation system for active
6 headsets, and more particularly to an active headset capable of compatibility with
7 existing socket configurations of an external device and capable of powering active
8 noise cancellation circuitry whether or not resident in the active headset.

9

10

2. Background of the Invention

11

12 In relatively noisy conditions such as the interior of an aircraft or other areas
13 where external noise interferes with the ability to hear a signal through a regular
14 headset, it is proposed practice to provide gain-adjustable active headsets wherein, at
15 least to some extent, the external noise is detected by a microphone, a noise
16 cancellation signal is generated and cancellation noise is propagated by an earphone to
17 prevent at least some of the external noise, especially lower frequency noise, from
18 reaching the ear. Such active headsets would substantially improve listening conditions
19 for the user compared to the currently provided passive headsets.

20

21 Normally, in the implementation of a stereo active headset, the circuitry
22 necessary for generation of the noise cancellation signal is either incorporated in the
23 headset, together with the microphone and earphone in each earpiece, or in a separate
24 box. All of the circuitry necessary for generation of the noise cancellation signal is thus
25 independent from the external device that is responsible for generating an audio signal

1 to the headset. The only connection required is then for the audio signal and this is
2 accomplished from either the headset or separate box with a standard audio lead
3 terminated with a 3.5mm stereo jack plug that plugs into the stereo socket of the
4 external device. The same type of socket is also used on computer sound cards. Such
5 external devices include the arm rest of an aircraft seat or consumer stereo equipment
6 such as a Walkman®. Headsets that incorporate the noise cancellation circuitry in the
7 headset, together with the microphone and earphone, and the use of a separate box
8 are relatively expensive.

9

10 Unfortunately, it is anticipated that, in an aircraft or in consumer stereo
11 equipment such as a Walkman®, rough handling by users might result in expensive
12 active headsets or in the need for separate boxes that might be easily damaged or
13 broken and, in some instances, stolen. In either case, costly replacement would be
14 necessary. Additionally, with active headsets, batteries must be used to power the
15 active circuitry, and this involves extra bulk and weight, a problem of particular
16 importance if everything is to be built into the headset. Although the power drain of a
17 well designed headset can be kept low enough to allow for an acceptable lifetime from
18 very small batteries, their cost and limited availability renders this an unpopular solution.
19 It would therefore be most advantageous if the means for powering the active headset
20 could be removed from the headset and located remotely therefrom.

21 It would additionally be advantageous if some or all of the noise cancellation
22 circuitry could be removed from the headset and located remotely. In an aircraft, the
23 noise cancellation circuitry could be remotely located in the arm rest of the passenger
24 seat. For consumer stereo equipment, the noise cancellation circuitry could be

remotely located within the consumer stereo equipment such as a Walkman® or on a computer sound card.

In addition to the cost and power concerns of separating the noise cancellation circuitry from the headset, there are compatibility concerns as well. Conventionally in an aircraft utilizing a passive headset, the wanted sound to be heard on the headset is transmitted via a stereo jack plug socket in the arm rest, and the headset is provided with a stereo jack plug which fits into this socket. Disadvantageously, conventional noise cancellation circuitry, if remotely located in the arm rest, would inherently require replacement of the stereo jack socket by an eight pin socket, due to the number of electrical connections which have to be made to the microphones and loudspeakers in the two earpieces of the headset. One proposal has already been put forward to provide such eight pin sockets in the arm rests, which would necessitate the provision of an expensive eight pin connector on an active headset.

In addition to aircraft applications, consumer stereo equipment that utilizes an active headset also has a stereo jack plug. The headset is provided with a stereo jack plug which fits into this socket. Disadvantageously, conventional noise cancellation circuitry, if remotely located within the stereo equipment, would also inherently require replacement of the stereo jack socket by an eight pin socket, due to the number of electrical connections which have to be made to the microphones and loudspeakers in the two earpieces of the headset.

Referring to **Figure 1**, a conventional bridge amplifier for the earphone in an active headset is shown. Two such bridge amplifiers are required, one for each

1 headphone, each with two terminals T_1 and T_2 connecting to noise cancellation
2 circuitry. I/P indicates the noise cancellation signal input to each bridge amplifier and
3 $\frac{1}{2}V_{cc}$ indicates one half the rail voltage (power supply voltage). Thus, if the noise
4 cancellation circuitry is remotely located, plug-in connectors are required to provide four
5 connections. Further, **Figure 3** shows a conventional headset arrangement having two
6 microphones, one in each earpiece L and R, and each with gain control provided by the
7 potentiometers (POT). Four terminal connections T_1 , T_2 , T_3 , and T_4 are required. It
8 would be advantageous to reduce the number of required connections between the
9 headset and electronics. This applies for both stereo headsets, just described, as well
10 as for mono headsets which may additionally employ the use of a boom microphone.

11
12 There is thus an unmet need in the art to provide noise cancellation circuitry
13 remote from the active headset that does not require replacement of the stereo jack
14 socket by an eight pin socket and whereby the number of connections required can be
15 reduced generally, for either mono or stereo headsets. There is also an unmet need in
16 the art to provide a means of powering the active headset that is removed from the
17 headset and located remotely therefrom.

SUMMARY OF THE INVENTION

It is an object of the invention to provide noise cancellation system having noise cancellation circuitry located remote from the active headset.

In another object of the invention that the active headset is provided with two stereo jack plugs or a six pin connector for connection to the noise cancellation circuitry.

It is another aspect of the invention that a mono active headset is provided with two mono jack plugs, one stereo jack plug or a 3 or 4 pin connector for connection to the noise cancellation circuitry.

It is another aspect of the invention that a mono headset with boom microphone is provided with a mono and stereo jack plug or a 4 or 5 pin connector for connection to the noise cancellation circuitry.

It is yet another object of the invention to provide a transient detector in the noise cancellation circuitry to overcome noise in the ear due to the generation of transients that occur when plugging in or unplugging the connector of the active headset.

1 It is further another object of the invention to reduce the number of necessary electrical
2 connections to the active headset by multiplexing the use of connectors that are already
3 present, thereby maintaining plug compatibility with normal headsets.

4

5 It is yet another object of the invention to provide a means of powering the active
6 headset that is removed from the active headset and located remotely therefrom.

7

8 Therefore, in accordance with the invention, the active headset is provided with
9 appropriate connectivity to provide compatibility with existing socket configurations. In
10 the case of a stereo headset, either two stereo jack plugs or a six pin connector are
11 provided. For a mono headset, two mono jack plugs, one stereo jack plug or a three or
12 four pin connector are provided and in the case of a mono headset with boom mic, one
13 mono and one stereo or a 4 or 5 pin connector may be used. The number of
14 connections between the active headset and the remote noise cancellation circuitry is
15 reduced from eight to six, five or four through the use of a common contact, having a
16 controlled impedance, that serves as an input connection to corresponding terminals of
17 the two earphones of the active headset. The appropriate arrangement of single-ended
18 operational amplifiers prevents roll-off at lower frequencies. Alternately, bootstrapped
19 emitter-follower transistors or operational amplifiers in the noise cancellation circuitry
20 behave as current sources at audio frequencies as well as provide correct bias voltage
21 to the microphones so that the common contact can serve as an input connection to
22 corresponding terminals of the two microphones in the earpiece.

23

24 According to another aspect of the present invention, the transients associated
25 with plugging in or unplugging stereo jack plugs or a six pin connector into an active

1 headset may be overcome by a transient detector in the noise cancellation circuitry.
2 The transient detector may comprise a window comparator and a mute logic circuit for
3 muting the output to the earphones if it exceeds a predetermined amplitude level.
4 Additionally, a decoupling capacitor in the active headset will overcome the plugging
5 in/unplugging transient noise problem and at the same time simplify the circuitry
6 because the bootstrapping and muting circuitry would not be required.

7
8 Yet another aspect of the present invention concerns the powering of the noise
9 cancellation circuitry whether the noise cancellation circuitry is placed inside the active
10 headset or inside a remote external device. Where the noise cancellation circuitry is
11 placed inside the active headset, the power necessary to power the headset and the
12 noise cancellation circuitry within it can be obtained from the external device that
13 generates an audio signal to the headset. Several approaches for providing power from
14 the external device that generates the audio signal to the headset include the following:
15 increasing the number of contacts on the connector; using a connector that employs a
16 single supply rail; using retractable contacts on the plug; using existing sockets on the
17 external device; using phantom powering techniques to power the active headset; using
18 a pulse width modulation (PWM) amplifier; and using the output audio signal power
19 produced by the external device to power the active headset.

20
21 Where, on the other hand, the noise cancellation circuitry is placed inside an
22 external device outside the headset, electrical connections must be made from the
23 external device to the headset. The number of connections on the socket could be
24 increased, but it is preferable to place some of the noise cancellation circuitry inside the
25 headset to reduce the required number of connections so that socket compatibility may

1 be maintained. First, the jack plug or socket on the external device may be used to
2 make electrical contact. Second, the drive signal to the headset earphone may be
3 converted into a PWM drive signal to power the microphone circuitry. Third, a
4 conventional linear audio signal may be superimposed upon a positive DC voltage level
5 that powers the microphone after the audio signal has been filtered. Fourth, a bridge
6 circuit is used to separate the microphone signal from the earphone drive signal and a
7 DC offset is added to the earphone drive signal to power the microphone circuitry.
8 Inside the headset, the DC offset is blocked from the earphone drive signal. This
9 technique requires that the earphone impedance be known in order to separate the
10 microphone signal from the earphone drive signal. The present invention describes
11 how headset identification could be used to identify the particular active headset model,
12 and therefore its impedance value, in addition to its presence. Fifth, a radio frequency
13 carrier signal may be used in a technique to combine the earphone and microphone
14 connections.

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BRIEF DESCRIPTION OF THE DRAWINGS

The novel features believed characteristic of the invention are set forth in the claims. The invention itself, however, as well as the preferred mode of use, and further objects and advantages thereof, will best be understood by reference to the following detailed description of an illustrative embodiment when read in conjunction with the accompanying drawings, wherein:

Figure 1 shows a bridge amplifier circuit, in accordance with the prior art for driving an earphone in an active headset;

Figures 2a, 2b and 2c are amplifier circuit diagrams that illustrate how the number of connections required for earphones may be reduced from four to three, in accordance with the present invention:

Figure 3 is a circuit diagram, in accordance with the prior art for headset microphones provided with gain control:

Figure 4a is a bootstrapping circuit diagram that provides a common contact from amplifiers in the noise cancellation circuitry to corresponding terminals of two earphones so that only three terminal connections are necessary between the microphones and the remote noise cancellation circuitry, in accordance with the present invention. **Figure 4b** is a mono version of the circuit.

1 **Figures 5a and 5b** show circuit diagrams that free the common contact of the
2 earphone(s) of the requirement to be at ground potential, in accordance with the
3 present invention;

4

5 **Figure 6** illustrates a transient detector circuit, according to a first embodiment
6 for suppressing noise transients in a headset of the present invention;

7

8 **Figure 7** illustrates a transient elimination circuit, according to a second
9 embodiment for suppressing noise transients in a headset of the present invention;

10

11 **Figure 8a and 8b** show a connector using a single supply rail to supply power to
12 the active headset, in accordance with a first embodiment of the present invention in
13 which the noise cancellation circuitry is placed inside the active headset;

14

15 **Figure 9** shows a connector using switch contacts to supply power to the active
16 heads, in accordance with an second embodiment of the present invention in which the
17 noise cancellation circuitry is placed inside the active headset;

18

19 **Figure 10** shows a connector using retractable contacts to supply power to the
20 active heads, in accordance with a third embodiment of the present invention in which
21 the noise cancellation circuitry is placed inside the active headset;

22

23 **Figure 11** shows an active headset plug configured with a rear power socket, in
24 accordance with a fourth embodiment of the present invention in which the noise
25 cancellation circuitry is placed inside the active headset;

1
2 **Figure 12** shows an active headset circuit configured with a power socket in the
3 rear of an active headset plug, in accordance with the fourth embodiment of the present
4 invention in which the noise cancellation circuitry is placed inside the active headset;
5

6 **Figure 13** shows a socket using an ultrasonic test tone to determine if supply
7 power is to be provided to the active headset, in accordance with a fifth embodiment of
8 the present invention in which the noise cancellation circuitry is placed inside the active
9 headset;

10
11 **Figure 14** shows a phantom powering circuit for providing power to the active
12 headset, in accordance with a sixth embodiment of the present invention in which the
13 noise cancellation circuitry is placed inside the active headset;

14
15 **Figure 15** shows a pulse width modulation circuit for providing power to the
16 active headset, in accordance with a seventh embodiment of the present invention in
17 which the noise cancellation circuitry is placed inside the active headset;

18
19 **Figure 16a** shows a circuit diagram of the output audio signal power produced
20 by the external device being used to power the active headset, in accordance with a
21 eighth embodiment of the present invention in which the noise cancellation circuitry is
22 placed inside the active headset;

23
24 **Figure 16b** shows a circuit diagram of the output audio signal power produced
25 by the external device being used to power the active headset using a switched-mode

1 power supply within the active headset to boost up the signal voltage level, in
2 accordance with a ninth embodiment of the present invention in which the noise
3 cancellation circuitry is placed inside the active headset;

4

5 **Figure 17** shows a linear charging circuit to reduce distortion in the audio
6 playthrough path when the output audio signal power produced by the external device
7 is used to power the active headset, in accordance with a tenth embodiment of the
8 present invention in which the noise cancellation circuitry is placed inside the active
9 headset;

10

11 **Figure 18** shows a schematic to eliminate the crosstalk between the earphone
12 and microphone signals associated with using a common ground for the earphones and
13 microphones due to the cable resistance, in accordance with a first embodiment of the
14 present invention in which the noise cancellation circuitry is located at least partially in
15 an external device remote from the active headset;

16

17 **Figure 19** shows a diagram illustrating automatically switching the line output
18 socket to serve an auxiliary purpose as an error microphone input socket when an
19 active headset is plugged in, in accordance with a second embodiment of the present
20 invention in which the noise cancellation circuitry is located at least partially in an
21 external device remote from the active headset;

22

23 **Figures 20a, 20b, and 20c** show a Pulse Width Modulated (PWM) drive to
24 power the microphone circuitry, in accordance with a third embodiment of the present

1 invention in which the noise cancellation circuitry is located at least partially in an
2 external device remote from the active headset;

3
4 **Figure 21** shows a conventional-linear audio signal superimposed upon a
5 positive DC voltage level to power the microphone by filtering off the audio signal and
6 measuring the output of the microphone by means of adding negative-going spikes, in
7 accordance with a fourth embodiment of the present invention in which the noise
8 cancellation circuitry is located at least partially in an external device remote from the
9 active headset;

10
11 **Figure 22** shows a bridge circuit used to separate out a microphone signal from
12 the headphone drive, in accordance with a fifth embodiment of the present invention in
13 which the noise cancellation circuitry is located at least partially in an external device
14 remote from the active headset; and

15
16 **Figure 23** shows a radio frequency carrier signal used to separate out a
17 microphone signal from the headphone drive, in accordance with a sixth embodiment of
18 the present invention in which the noise cancellation circuitry is located at least partially
19 in an external device remote from the active headset.

20
21 **Figure 24a** shows a dual mono jack plug connector for use with a mono active
22 headset with electronics separate from the headset and with earphone, microphone
23 and gain adjustment element in the headset. The pin connections to the headset
24 elements are also indicated.

Figure 24b shows a stereo jack plug connector for use with a mono active headset with electronics separate from the headset and with earphone, microphone and gain adjustment element in the headset. The pin connections to the headset elements are also indicated.

Figure 25 shows a dual mono plus stereo jack plug connector for use with a mono active headset incorporating a boom microphone assembly with electronics separate from the headset and with earphone, microphones and gain adjustment element in the headset. The pin connections to the headset elements are also indicated.

DESCRIPTION OF THE INVENTION

The present invention describes a noise cancellation system for an active headset in which the number of required connections between noise cancellation circuitry to the active headset is reduced. The same principles of the invention for reducing the number of required connections are applicable to both stereo active headsets as well as mono active headsets, which may be used, for example, in telephone call centers. A mono headset may be employed in order that the wearer is less isolated from the environment. The same advantages still hold in terms of reducing the number of required connections between the headset and electronics. In the case of the mono headset, reducing the number of connections for the active headset also allows a boom microphone to be incorporated into the headset whilst still being able to use a standard connector. Even with a stereo headset with boom microphone, reduction of the necessary connections is still advantageous.

The invention is generally concerned with noise cancellation using a headset including, but not limited to, noise cancellation systems in aircraft and consumer stereo equipment such as Walkmans®, wherein each user has an associated active headset.

Assuming the gain in the headset is adjustable so that the active headset is usable in any number of applications regardless of manufacturing tolerances in the remote noise cancellation circuits, a minimum of three electrical connections would normally be required for stereo earphones, left, right and common, and a minimum of four electrical connections would normally be required for electret-type microphones, namely left, right, common and power. While in theory the two common lines could be

1 joined together, in practice this creates a significant common impedance between the
2 loudspeakers and the microphones, leading to loss of stability and/or loss of external
3 noise cancellation and/or to commoning of the two channels, especially with low
4 impedance loudspeakers. Moreover, if, as would be preferred in order to prevent roll-
5 off at the lower end of the frequency range of external noise cancellation, the
6 earphones are to be driven by bridge amplifiers, four electrical connections would be
7 required for earphones being thus configured.

8

9 There exist several possibilities for reducing to six the number of electrical
10 connections between the active headset and the remote noise cancellation circuitry,
11 thus enabling the use of two stereo jack plugs, or a six pin connector, to provide the
12 necessary connections. Furthermore, assuming the use of transformers is to be
13 avoided, standard "phantom powering" techniques that would require additional circuitry
14 be added to the active headset could be used. Several other possibilities exists for
15 reducing the number of connections between the headset and remote noise
16 cancellation circuit to six.

17

18 First, the number of electrical connections required for the earphones is reduced
19 to three by controlling the impedances of the earphones, so that a common contact
20 from amplifiers in the remote noise cancellation circuitry can serve as an input
21 connection to corresponding terminals of the two earphones. While one advantage of
22 bridge amplifier circuitry is lost, i.e. the double swing in amplification, the avoidance of
23 roll off at lower frequencies is achievable by coupling two single-ended operational
24 amplifiers via a third operational amplifier at which the common third terminal is
25 provided, or by grounding through a common terminal one side of both earphones and

1 either supplying the earphones with the noise cancellation signals from single-ended
2 operational amplifiers via suitable capacitances or by connecting two single-ended
3 operational amplifiers between positive and negative power supply voltages. This first
4 arrangement is preferable when only a single power supply is available.
5

6 In the arrangement of **Figure 2a**, an amplifier circuit is shown in which the
7 impedances of the earphones are controlled, reducing the number of terminal
8 connections from four to three. A third operational amplifier 22 is connected between
9 two single ended operational amplifiers 24, 26, one for each earphone. L and R
10 indicate the left and right noise cancellation signals input to the respective
11 amplifiers 24, 26. T_1 , T_2 and T_3 are the three connection points between the
12 headphones 28, 30 and the remote noise cancellation circuitry.
13

14 **Figures 2b** and **2c** show two alternative circuits which may be used in place of
15 the circuit of **Figure 2a**. In each case, one side of the two earphones 28, 30 is
16 grounded. In **Figure 2b**, the noise cancellation signals are fed to the earphones via
17 suitable capacitances 42, 44. The arrangement of **Figure 2c** requires the availability of
18 a negative power supply (-V). The three connection points between the headphones
19 28, 30 and the remote noise cancellation circuitry are again indicated as T_1 , T_2 and T_3 .
20

21 In the case of a mono headset, it is of course not possible to make use of a
22 common connection between the earphones - there is only one earphone and that
23 requires two connections.
24

1 Second, the number of connections required for the earphones may be reduced
2 by configuring bootstrapped emitter-follower transistors in the remote control circuitry to
3 not only act as current sources at audio frequencies but also to provide correct bias
4 voltage to the microphones, whereby a common contact can serve as an input
5 connection to corresponding terminals of the two microphones in the earpiece.
6 Alternatively, operational amplifiers may be used instead of emitter-follower transistors.
7

8 **Figure 4a** is a circuit diagram for a circuit in the remote noise cancellation
9 controller 71 for handling the signals from the microphones in the headset in
10 accordance with the invention, wherein emitter follower transistors 72, 74, replaceable
11 by operational amplifiers if desired, act as current sources at audio frequencies by
12 virtue of bootstrapping capacitors 76, 78, while simultaneously providing the correct
13 bias voltages to the microphones 80, 82. The circuitry of remote noise cancellation
14 controller 71 is inside the dashed lines of Figure 4. The sensitivity of each microphone
15 remains fully adjustable by potentiometers 84, 86, and only three terminal connections
16 T_4 , T_5 and T_6 are necessary between the microphones and the remote noise
17 cancellation controller 71. V_{cc} indicates the rail voltage, V_B , a bias voltage.
18

19 Figure 4b shows the circuitry that would be required for the mono version of such
20 a headset. Only two terminal connections T_4 and T_5 are necessary between the
21 microphone and the remote noise cancellation controller 71.
22

23 The only minor disadvantages of the arrangements above-described are an
24 increase in current drawn and a slightly reduced bandwidth in noise cancellation in
25 order to maintain stability.

1
2 **Figure 5a** shows an alternative arrangement in accordance with the present
3 invention. This arrangement is based on the concept of freeing the common line of the
4 earphones from any requirement to be at ground potential. The output from the power
5 amplifier in the noise cancellation circuitry is capacitatively coupled to remove DC
6 offset. The common earphone connection is then made at T_1 . The common line for the
7 earphones 28, 30 is then connected to the power supply line to the microphones 80, 82,
8 so that the common line assumes a DC potential with respect to the microphone
9 ground. A few inexpensive additional components are required in the headset to filter
10 off any signal voltage in the supply. As indicated, a resistor 92 and a capacitor 94
11 would be required, and possibly in some cases a zener diode. These elements could
12 be located on the printed circuit board which carries the sensitivity potentiometers 84,
13 86.
14

15 The overall arrangement would necessitate only six terminal connections T_1 , T_2 ,
16 T_3 , T_4 , T_5 and T_6 between the headset and the noise cancellation circuitry, as indicated
17 in **Figure 5**. The bootstrapping circuit of **Figure 4** is no longer required.
18

19 Figure 5b shows the same approach used with a mono headset, but instead of
20 the common connection between the earphones being tied to terminal T_1 only one of
21 the terminals of the mono earphone is tied to it instead.
22

23 Once the number of electrical connections between the active headset and the
24 remote noise cancellation circuitry has been reduced from eight to six, compatibility with
25 existing stereo jacks or six pin connectors is assured. The noise cancellation circuitry

1 may then be provided within an external device and the external device provided with a
2 six contact socket or two stereo jack plug sockets. In the latter case, preferably one
3 socket provides for three input connections to the gain adjustable microphones in the
4 earpieces and one stereo jack plug socket provides three input connections to the
5 earphones in the earpieces and thus is alternatively usable to plug in a conventional
6 passive headset.

7

8 The arrangements above-described enable the use of two stereo jack plugs to
9 connect an active headset to remote noise cancellation circuitry. Thus, in an aircraft,
10 the noise cancellation circuitry can be located in the arm rest of a seat, which arm rest
11 is provided with two stereo jack plug sockets for receiving two stereo jack plugs
12 provided on the active headset.

13

14 In the case of a mono headset, the techniques described above can be used to
15 advantage also as shown in Figures 24a and 24b. Figure 24a illustrates how a dual
16 mono jack plug could be used to provide for connection between the headset and
17 remote electronics. Figure 24b shows the connections when using a single stereo jack
18 plug. In this case one of the microphone connections and one of the earphone
19 connections are commoned. This could lead to crosstalk problems unless the common
20 resistance is low.

21

22 Figure 25 shows a possible connection arrangement if a boom microphone is
23 incorporated in the active headset, using a mono and stereo jack plug.

1 Another problem which can arise when the noise cancellation circuitry is remote
2 from the headset is that because power is necessary for the microphones, there is a
3 DC offset at the microphone terminals, larger than the signal voltages, that can give rise
4 to a large transient in the headphones when plugging in and unplugging the stereo jack
5 plugs (or six pin connector). This can be overcome by the provision of a transient
6 detector in the noise cancellation circuitry. For example, the transient detector may
7 comprise a window comparator and a mute logic circuit for muting any output to the
8 earphones that exceeds a predetermined amplitude level.

9
10 In order to avoid a "plop" noise in the ear when the stereo jack plugs are plugged
11 in and unplugged when the headset is being worn, a transient detector and suppressor
12 may be provided in the noise cancellation circuitry. As shown in **Figure 6**, in which the
13 noise cancellation circuitry includes a window comparator 102 and a mute logic circuit
14 104 for muting signals to the earphones 28, 30 if the amplitudes of those signals
15 exceeds a predetermined value. The circuitry shown is that for one earphone, for
16 example the left earphone. RHC indicates the connection leading to a similar circuit for
17 the right earphone. As before V_{CC} indicates the rail voltage (power supply) and V_B , a
18 bias voltage.

19
20 The removal of the plug-in transient is relatively straightforward. Before the
21 headset is plugged in, the microphone connection point is left open circuit and so no
22 current flows in the emitter follower transistor, and the bootstrapping capacitor is
23 discharged. The voltage at the microphone input is at V_B under these conditions.
24 When the microphone makes connection, the voltage is initially pulled down to ground
25 since the bootstrapping capacitor cannot instantaneously charge up to allow current to

1 flow in the transistor. This transient would normally get through to the earphone.
2 Gradually the capacitor charges up and re-establishes the correct voltage on the
3 microphone and the correct current in the transistor. The mute circuit keeps the power
4 amplifier disabled until it detects that the current has passed a predetermined threshold,
5 indicating that the microphone is now in circuit, and then only after a set delay is the
6 power amplifier enabled. In this way the transient has disappeared before the power
7 amplifier becomes active.

8

9 When the headset is unplugged there is a transient whose direction depends on
10 the geometry of the plug and socket. If a clean break occurs then the transient is
11 positive due to the transistor attempting to maintain the previous current flow until the
12 bootstrapping capacitor discharges. If, however, the plug momentarily shorts the
13 microphone input to ground as it is withdrawn, the transient is negative. These
14 transients have a steep edge which is fed through to a window comparator 102 which
15 detects whether the comparison result exceeds a predetermined positive or negative
16 threshold. If the thresholds are exceeded, the power amplifier is rapidly muted. In this
17 way the circuit can differentiate between disconnection transients and the normal signal
18 voltages that are present at the input.

19

20 This transient elimination circuitry is also applicable to the mono headset case, in
21 which the transient detection circuit may be connected to the microphone in the
22 headset used for active noise reduction. Although a stereo jack plug is shown in Figure
23 6, this is simply by way of illustration - the operation of the transient elimination circuitry
24 is independent of the style of connector.

1 It should be noted that if the circuit of Figure 5, rather than the bootstrapping
2 circuit of Figure 4, were used in conjunction with the transient detector circuit of Figure
3 6, then the portion of Figure 6 applicable to plugging-in transients could be removed.
4

5 Alternately, it is possible to provide a decoupling capacitor across the power
6 supply to the microphone in each earpiece, with a DC blocking capacitor between the
7 microphone output and the signal lines. This solution simplifies the remote noise
8 cancellation circuitry in return for the introduction of a few inexpensive components into
9 the active headset and boot strapping circuitry or muting circuitry becomes
10 unnecessary.

11
12 As shown in the transient elimination circuit of **Figure 7** for one earphone 28, the
13 addition of decoupling capacitor 112 and AC coupling capacitor 114 in the headset will
14 overcome the plugging in/unplugging problem. When the headset is plugged into or
15 unplugged, the power applied to the microphone circuit charges or discharges the
16 microphone power supply decoupling capacitor 112 only slowly through the series
17 resistor 118. The change in voltage on the microphone output is too slow to pass
18 through the AC coupling capacitor 114 and so does not cause a transient on the input
19 to the following circuitry. The use of a transient detector in the noise cancellation
20 circuitry, whether of the kind shown in **Figure 6** or otherwise, is thereby avoided, but
21 some inexpensive components are added to the headset, as indicated by the
22 capacitors 112, 114 already referred to, and resistors 116 and 118. These additional
23 components, while being required in the headset for each earphone, simplify the noise
24 cancellation circuitry in that the bootstrapping circuitry of **Figure 4** and the muting
25 circuitry of **Figure 5** are no longer necessary. It is further noted that the headphone

1 common and microphone common shown in Figure 7 are terminals of the circuitry; in
2 the case of a mono headset, these would be referred to as "H/P TERMINAL" and "MIC
3 TERMINAL," for instance.

4

5 When the active noise cancellation circuitry is placed inside the active headset,
6 power must be provided to the headset for the active noise cancellation circuitry. The
7 power for the active headset could be obtained from the external device that is
8 responsible for generating an audio signal to the headset. Several means are available
9 to provide power to the active headset from the external device: the number of contacts
10 on the connector may be increased to supply power to the active headset; a connector
11 using a single supply rail may be used to supply power to the active headset;
12 retractable contacts on the plug may be used to supply power to the active headset;
13 sockets on the external device, such as the external power socket or the line out socket
14 for low level audio signals, may be used to power the active headset; phantom
15 powering may be used to power the active headset; a pulse width modulation (PWM)
16 amplifier may be used to power the active headset; or the output audio signal power
17 produced by the external device may be used to power the active headset.

18

19 Of the above, the simplest method is to modify the connector to increase the
20 number of contacts. Jack plugs and sockets exist for telephony applications which have
21 additional connection rings over those used for stereo applications. This approach
22 could therefore be easily used for supplying power to the headset.

23

24 One technique to increase the number of contacts is to implement an extra
25 contact beyond the distance that a stereo plug would extend into the socket so that a
26 stereo plug would not make electrical contact with it. This extra contact carries the

1 power needed to power the noise cancellation circuitry, the supply earth being
2 combined with the signal earth. Compatibility with a normal non-active headset is
3 retained since a normal non-active headset plug would not extend far enough into the
4 socket to connect with the power contact.

5

6 Referring to **Figure 8**, a connector 122 using a single supply rail to supply power
7 to the active headset is shown. Implementing a single supply rail for the headset
8 requires an extra contact 124 beyond the distance that a stereo plug would extend into
9 the socket. This extra contact carries the power, the supply earth being combined with
10 the signal earth. Compatibility with a normal non-active headset is retained since a
11 normal non-active headset plug would not extend far enough into the socket to connect
12 with the power contact. The active headset would use an extra long plug with an extra
13 contact to pick up the power.

14

15 The active headset also retains compatibility with normal headphone sockets
16 since normal headphone sockets usually are open at the end or longer than the plug
17 length. The extra length of the plug would thus extend harmlessly beyond the normal
18 contacts. The active headset circuit passes an audio signal straight through to the
19 headphone when there is no power supply present to activate the cancellation circuitry.
20 If the length of the plug does prove to be a problem with some sockets, then an adapter
21 could be supplied with the active headset so that it can be plugged into normal sockets.

22

23 A second technique to increase the number of contacts is to implement a
24 connector using switch contacts to supply power to the active headset. As shown in
25 **Figure 9**, a connector uses switch contacts 132 to supply power to the active headset.

1 As the switch is moved between “normal” and “active” positions the contact end of the
2 plug extends and at the same time switch contacts within the plug housing reconfigure
3 the connections to take care of routing the signals correctly.

5 These techniques could be extended if dual rails are required, but the extra
6 length needed to accommodate two extra contacts could be problematic unless the
7 switchable plug approach was used to ensure compatibility.

9 A third technique to increase the number of contacts is to implement a connector
10 using a retractable contacts to supply power to the active headset. Compatibility with a
11 normal non-active headset socket is retained since the pins either retract automatically
12 when the plug is inserted into a normal non-active headset socket, or can be retracted
13 manually.

15 Alternatively, the plug could be made into an alterable format as shown in
16 **Figure 10**, a connector using a retractable contacts to supply power to the active
17 headset. Retractable thin pins 142 are added around the periphery of a standard jack
18 plug. These pins are small enough so as to cause only a minimal increase on the
19 socket diameter. The plug is designed such that the pins are retractable in a non-active
20 mode. Compatibility with a normal non-active headset socket is retained since the pins
21 either retract automatically when the plug is inserted into a normal non-active headset
22 socket, or can be retracted manually.

24 An alternative to expanding the number of contacts on the headphone socket is
25 to make use of other sockets that already exist on the external device. If the external

1 device is optionally battery-powered and has an external power socket such as a
2 Walkman®, for instance, then the external power socket may be used. The external
3 power socket is replaced with a dual function socket that disconnects the batteries if a
4 normal plug is inserted, but connects the batteries if a special active headset plug is
5 used thereby powering the noise cancellation circuitry. Alternatively, the active headset
6 plug may be configured with a power socket in the rear of the headset plug so that the
7 external device could still be used with an external power supply when the active
8 headset is being used.

9

10 The techniques illustrated in Figures 8 through 10 could also be used in the case
11 of a mono headset with boom microphone, such as when used with a computer for
12 voice recognition purposes, by replacing the connection to the right-hand channel of the
13 headset with a connection to the boom microphone. The connection previously made to
14 the left channel is now the signal input for the mono signal to the earphone(s).

15

16 Normally the external power socket is designed to act as a power input socket
17 and therefore disconnects the batteries when a plug is inserted in order to avoid the
18 power supply damaging the batteries. As shown in **Figure 11**, the external power
19 socket is replaced with a dual function socket 152 that disconnects the batteries if a
20 normal plug is inserted, but connects the batteries if a special active headset plug is
21 used. This may be accomplished by extending the length of the external power socket
22 and adding an extra contact 154 that is always connected to the battery. Alternatively,
23 the external power socket may be implemented with the battery disconnect function
24 being performed electronically instead of with a physical battery disconnect switch. In
25 this configuration (**Figure 12**), the battery 162 is connected to the external power socket

1 and the external device circuit via a diode 164, and likewise the external power supply
2 166 is connected via a diode 168 to the power supply plug. In this way, whichever
3 voltage is the larger will determine which power source supplies the circuitry. Thus, no
4 current will be able to flow into the battery to cause damage.

5
6 The active headset plug 156 may be configured with a power socket in the rear
7 so that the external device could still be used with an external power supply when the
8 active headset is being used as shown in **Figures 11**. In this configuration, the active
9 headset plug would connect directly to the headset power supply and the external
10 power socket on the external device.

11
12 Other sockets on the external device may be used to power the active headset.
13 If the external device has a line-out socket for low level audio signals, then it may be
14 used to power the active headset. The function of the line-out socket is electronically
15 switched over to supply power instead of an audio signal when an active headset is
16 plugged into the socket. As shown in **Figure 13**, a socket uses an ultrasonic test tone
17 to determine if supply power is to be provided to the active headset. In this
18 configuration, the function of the line out socket 172 is electronically switched over to
19 supplying power instead of an audio signal when an active headset is plugged into the
20 socket. This may be implemented by a sensing system that superimposes an
21 ultrasonic test tone from generator 174 onto the audio output when a headset plug 184
22 is first inserted. An ultrasonic test tone would be generated whenever the normally
23 closed earth contact switch 176 on the line out socket 172 is opened due to insertion of
24 the headset plug. In this configuration, the active headset is arranged to present a
25 specific impedance to the ultrasonic test tone signal by means of network 178,

1 whereupon, controlled by detector 180 and logic 182, the function is then switched over
2 to supplying power to the active headset.

3
4 Phantom powering techniques may also be used to power the active headset
5 using the same wires as are used for the audio signal thus requiring no additional
6 connections on the headset plug and socket. The power supply voltage is supplied
7 directly to the headset socket with the audio signal voltage superimposed on top of the
8 power supply voltage by means of a summer. The power supply voltage is then low-
9 pass filtered to strip away the audio before being fed to the headset circuit power
10 supply system. Phantom powering is a technique that is used in music and PA systems
11 for powering microphones using the same wires as are used for the audio signal. This
12 technique may be used for powering active headsets thus requiring no additional
13 connections on the headset plug and socket.

14
15 In the phantom powering circuit of **Figure 14**, the power supply voltage is
16 supplied directly to the headset socket with the audio signal voltage superimposed on
17 top of the power supply voltage by means of summer 192. The power supply lines on
18 the active headset are high pass coupled into the headset audio input with capacitor
19 194 and inductor 196, thus blocking the DC power and recovering the audio. The
20 power supply voltage is then low pass filtered by inductor 198 and capacitor 200 to strip
21 away the audio before being fed to the headset circuit power supply system.

22
23 To retain compatibility with normal headsets, a means must be employed to
24 detect a normal headset so that the power supply voltage can be removed from the
25 socket to prevent damage to the normal headset. The technique described above of

1 implementing a ultrasonic test tone signal may be used to remove the power supply
2 voltage whereby the active headset is arranged to present a specific impedance to the
3 ultrasonic test tone signal to determine if the power supply voltage should be supplied.

4
5 Yet another alternative is to use a pulse width modulation (PWM) amplifier to
6 power the active headset. The PWM amplifier produces a waveform that has a square
7 wave with a frequency that is much higher than the highest desired audio frequency.
8 The mark-to-space ratio, the ratio of the time the square wave is at a high positive
9 voltage to the time it is at a low or high negative voltage, is made proportional to the
10 amplitude of the audio signal. The mark-to-space ratio is unity in the absence of
11 modulation. The average strength of the waveform is proportional to the modulating
12 signal, such that when the high frequency square wave carrier is filtered off, the audio is
13 recovered intact. This happens naturally in a conventional headset by a combination of
14 the inherent inductance of the voice coil and the acoustic roll-off of the earphone. Thus,
15 compatibility is retained with conventional headphones. The PWM output signal may
16 be automatically disabled upon detection of a normal headset.

17
18 **Figure 15** shows such a pulse width modulation circuit. The pulse width
19 modulation amplifier 212 produces a waveform that has a square wave with a
20 frequency that is much higher than the highest desired audio frequency. When an
21 active headset is plugged in, the squarewave is rectified and filtered using diodes 214
22 and 216 and capacitors 218 and 220 to produce the power to drive the circuitry. The
23 audio is recovered by low pass filtering the incoming squarewave with inductor 222 and
24 capacitor 224 and feeding it to the audio input. One potential problem with this
25 technique is that cable resistance may cause the recovered audio signal to become

distorted due to the current drawn by the active headset circuit causing voltage drops across the cable resistance. This may be rectified by passing the squarewave through a limiter, formed in this instance by resistor 226 and diodes 228 and 230, before reaching the low pass filter to eliminate the distortion.

The PWM output signal may be automatically disabled upon detection of a normal headset. This is desirable to reduce the interference to other equipment caused by the high frequency nature of the squarewave causing radio frequency radiation from an unscreened headphone cable. The rectification and smoothing process within the active headset causes a different current waveform profile than a normal headset. This information may be used either to revert to a conventional power amplifier for the normal headset or to switch in a low pass filter that removes the squarewave but leaves the audio content intact. Thus, the presence of an active headset may be inferred from the current drawn from the power supply.

The output audio signal power produced by the external device may also be used to power the active headset. If the external device produces an output audio signal power sufficient to power an active headset, then it may be used to power the active headset. Deriving the power for the active circuitry from the output audio signal would not require any modification to the connectors or changes in the external device circuitry, other than to ensure that the available signal voltage of the output audio signal is sufficient to additionally power the active headset. The output audio signal from the external device is turned up to the fullest volume commensurate with the power amplifier not clipping. The audio signal is then rectified and at audio signal peaks the output audio signal charges a reservoir capacitor. The power for the active headset is

1 derived from the audio signal at audio signal peaks and from the reservoir capacitor
2 otherwise. A switched-mode power supply may be added within the active headset to
3 boost the signal voltage level of the output audio signal so that the battery or the
4 reservoir capacitor can be charged even at low audio signal levels. The audio signal
5 input to the noise cancellation system would be derived from an attenuated version of
6 the power signal used to activate the headset circuitry. A dummy load may be switched
7 across the signal line at low signal voltage and removed at higher voltages to obliterate
8 distortion in the audio path introduced by any significant resistance in the headset
9 cable.

10

11 The output audio signal power produced by the external device may be used to
12 power the active headset as shown in **Figures 16a and 16b**. If the external device
13 produces an output audio signal power sufficient to power an active headset, then it
14 may be used to power the active headset. For instance, typically the output power that
15 can be produced from a Walkman® is higher than that needed to drive a well designed
16 active headset. Deriving the power for the active circuitry from the output audio signal
17 would not require any modification to the connectors or changes in the external device
18 circuitry, other than to ensure the available signal voltage was high enough for the
19 intended purpose.

20

21 Referring to **Figure 16a**, the audio signal from the external device is turned up to
22 the fullest volume commensurate with the power amplifier not clipping. Diode 242 in
23 the active headset rectifies the audio signal and at audio signal peaks the audio signal
24 is used to charge a reservoir capacitor 244 through current limiting resistor 246. The
25 power for the active headset is derived from the audio signal at audio signal peaks and

1 from the reservoir capacitor otherwise. To prevent the likelihood that the circuit would
2 run out of power during quiet passages in the music, the reservoir capacitor may be
3 replaced with a small rechargeable battery which would operate like a very large value
4 storage capacitor. Provided the external device power amplifier has a larger voltage
5 output capability than that necessary to operate the active headset, then the battery can
6 be charged even with the average value of the audio signal rather than just the peaks.

7

8 As shown in **Figure 16b**, a switched-mode power supply formed by elements
9 252-260 may be added within the active headset to boost up the signal voltage level so
10 that the battery or reservoir capacitor can be charged even with low audio signal levels.
11 The audio signal input to the system would be derived from an attenuated version of the
12 power signal used to activate the headset circuitry.

13

14 Any significant resistance in the headset cable may introduce distortion into the
15 audio playthrough path when the output audio signal power produced by the external
16 device is used to power the active headset. This arises because the current drawn
17 from the audio power is not a linear function of level. At low signal levels there is
18 insufficient voltage present to charge the storage system, even when using a switched-
19 mode power supply, and thus no current will be drawn from the audio. Since it is the
20 voltage at the end of the cable that is bled off to provide the audio playthrough
21 component, the voltage drop across the cable will therefore only occur at the higher
22 signal voltages thereby producing the distortion. To alleviate this problem, a dummy
23 load, may be switched across the signal line at low signal voltage and removed at
24 higher voltages. Thus, the total load resistance presented across the cable is signal
25 independent and the distortion is reduced. Such a scheme for a linear charging circuit

1 is shown in **Figure 17**. When the current through the battery falls below a preset level
2 of V_{ref}/R_1 , the comparator 272 switches on transistor 274 to place resistor 276 across
3 the signal line. The value of resistor 276 is chosen to maintain the same input
4 resistance at low signal levels as exists at high levels.

5
6 A related solution that also uses the output audio signal power borrows from the
7 PWM approach previously discussed. A high-frequency pulse train superimposed on
8 the power amplifier output charges up the reservoir capacitor. The pulses would be of
9 full amplitude at an inaudibly high frequency. The audio signal would be arranged to be
10 always lower in amplitude than the pulses. By keeping the pulse width narrow, the
11 interference is kept to a minimum even with unscreened cables, but there would still be
12 sufficient pulse width to replenish the reservoir capacitor charge. This scheme would
13 reduce the effects of cable resistance on audio signal distortion.

14
15 When at least some of the active noise cancellation circuitry is remotely located
16 inside an external device, such as inside a Walkman® or other electrical connections
17 must be made to the headset for the earphones and the error sensing microphones.
18 This requires potentially more connections and involves additional problems due to the
19 low signal level from the error microphones. The simplest approach is to increase the
20 number of connections on the socket. However, it is possible and desirable, by putting
21 some circuitry inside the active headset, to reduce the required number of electrical
22 connections to three and at the same time maintain compatibility with normal passive
23 headsets. These various techniques are described below.

1 The jack plug or a socket on the external device may be used as contacts to the
2 active headset while still retaining compatibility with normal passive headsets. If an
3 additional socket is not available on the external device then one of the sockets already
4 present on the external device may be used to serve an auxiliary purpose when an
5 active headset is plugged in.

6
7 It has been described above how the jack plug and socket can be altered to
8 accommodate an extra contact while still retaining compatibility with normal headsets.
9 The minimum number of contacts needed for a straightforward connection regime to
10 the headset is five if the microphones and earphone all share a common ground. This
11 requires two extra contacts on the jack plug resulting in a jack plug that is too long to fit
12 into a standard stereo jack socket. The switchable jack plug approach shown in Figure
13 9 would still be useable. In addition, extra peripheral pins could also be used for the
14 microphone connections in the same manner as outlined for transferring power to an
15 integrated active headset.

16
17 A potential problem associated with using a common ground for the earphones
18 and microphones is crosstalk between the earphone and microphone signals due to the
19 cable resistance. The crosstalk between the earphone and microphone signals due to
20 the cable resistance may be eliminated if the cable resistance is known as is shown in
21 **Figure 18.** A resistor R_x is placed between the cable common ground wire and circuit
22 ground. The voltage drop across this resistor is sensed and buffered by amplifier 282
23 and subtracted from the microphone output, thus eliminating the crosstalk.

1 Other sockets on the external device may be used as contacts to the active
2 headset as is shown in **Figure 19**. As described above, if an additional socket is not
3 available on the external device then one of the sockets already present on the external
4 device may be used to serve an auxiliary purpose when an active headset is plugged in.
5 For instance, the line output socket 172 may double as the error microphone input
6 socket. In this configuration, the line output socket is switched automatically by Relay
7 of Figure 19 by interrogating the headphone output socket with an ultrasonic tone in the
8 same manner as described for **Figure 13**. The active headset presents a pre-
9 determined impedance at this frequency causing the mode of the line socket to be
10 switched over.

11 By including some of the signal conditioning circuitry within the headset, it is
12 possible to reduce the required number of contacts to three even when the active
13 circuitry is contained within the external device. This technique is most applicable for
14 higher performing active headsets where a much more complex equalizer might be
15 needed. In this instance, it would be preferable to keep most of the electronics inside
16 the external device. The various methods for combining the earphone and microphone
17 connections are described below.

19 The drive signal to the headset earphone may be converted into a Pulse Width
20 Modulated (PWM) drive signal to power the microphone circuitry. Power for the
21 microphone can be derived in a similar manner to the technique described above to
22 supply an integrated active headset, without corrupting the drive for the earphone.
23 Compatibility is retained with a normal headset because of the bi-directional nature of
24 the PWM drive signal. Since a conventional headset lacks the internal diodes, the
25

1 PWM is presented to the conventional headset without a DC offset and the carrier
2 signal is filtered out by a combination of the earphone inductance, the natural high
3 frequency roll-off of the headphone frequency response, and the limited hearing range
4 of the ear. Alternatively, the output may be switched from the PWM amplifier to a
5 conventional linear amplifier.

6
7 The drive signal to the headset earphone may be converted into a Pulse Width
8 Modulated (PWM) drive to power the microphone circuitry as shown in the simplified
9 circuits in **Figures 20a, 20b, and 20c**. The constant height of the square wave signal,
10 combined with the fact that even when the modulating audio signal is at zero the square
11 wave is still present, is ideal. Power for the microphone can be derived in a similar
12 manner to the technique described above to supply an integrated active headset,
13 without corrupting the drive for the earphone.

14
15 In **Figure 20a** a bi-directional PWM square wave is connected to the earphone
16 through diodes 302 and 304 such that the earphone is only connected in circuit when
17 the square wave is at the negative voltage level. This causes a DC offset on the
18 earphone, but will not otherwise affect the recovered audio, and the DC offset can be
19 removed by capacitor coupling the earphone with capacitor 306. When the square
20 wave is at a negative voltage it is also used to charge a reservoir capacitor 308 via
21 diode 310 to power the error microphone. Only minimal power is necessary to charge
22 the reservoir and thus does not appreciably affect the signal sent to the earphone.
23 When the square wave is at a positive voltage it is isolated from the earphone but
24 connected by means of a further diode 312 to the microphone output circuit 314 such
25 that the microphone signal level can be read during this period by means of a current

1 sensing resistor 332 in the output drive line in the external device, as shown in **Figure**
2 **20c**. It is also possible to dispense with the need to continuously power the
3 microphone and only connect it into circuit for the periods that the square wave is
4 positive thus saving components in the headset as in **Figure 20b**. Diodes 322 and 324
5 and capacitor 326 in **Figure 20b** perform the same purpose as elements 302, 304 and
6 306 in **Figure 20a**. Diode 328 only connects the microphone in circuit during the
7 positive going period of the squarewave.

8
9 Compatibility is retained with a normal headset because of the bi-directional
10 nature of the PWM drive signal. Since a conventional headset lacks the internal
11 diodes, the PWM is presented to the conventional headset without a DC offset and the
12 carrier signal is filtered out by a combination of the earphone inductance, the natural
13 high frequency roll-off of the headphone frequency response, and the limited hearing
14 range of the ear.

15
16 The symmetry of the current drawn during positive and negative half cycles of
17 the squarewave at times when the mark to space ratio is unity is monitored to detect
18 when a normal headset is plugged in to alter the equalization applied to the audio
19 signal. When a normal headset is plugged in, the current flow is symmetrical, but when
20 an active headset is plugged in, the current flow is not symmetrical. As well as
21 switching the equalization, an output filter may be switched into place when a normal
22 headset is detected in order to reduce radio frequency radiation. Alternatively, the
23 output may be switched from the PWM amplifier to a conventional linear amp.

1 Another alternative uses a conventional analog output by superimposing a
2 conventional-linear audio signal upon a positive DC voltage level configured such that
3 the combined voltage level never goes below ground as shown in **Figure 21**. The DC
4 level is used to power the microphone by filtering off the audio signal with resistor 342
5 and capacitor 344. When ever the voltage level is above ground, P-channel FET 348 is
6 held on so that the signal is connected through to the earphone. As before the
7 earphone can be capacitor coupled within the headset to remove the DC offset.

8
9 To read the microphone output signal, very short duration high frequency
10 negative-going spikes are superimposed upon the drive signal. For the period of the
11 negative spike, the earphone is temporarily disconnected from the power amp as Field
12 Effect Transistor (FET) 348 turns off and the signal line is instead connected to the
13 microphone. Since the spikes are a very high frequency they are not audible in the
14 earphone. A current sensing resistor 350 is used in series with the power amp 352 with
15 FET 354 used to short it out when the signal level is positive. In this way a higher value
16 can be used for resistor 350 to produce a higher microphone output signal without
17 causing signal loss to the earphone 356.

18
19 To maintain compatibility with a normal headset in this configuration, the DC
20 offset applied to the headset must be eliminated when a normal headset is plugged in.
21 This may be accomplished by increasing the DC voltage slowly from zero whenever a
22 headset is connected. The DC voltage is increased to its full level only if the presence
23 of a microphone is detected, by measuring whether current is drawn during the period
24 of the negative-going spikes. Otherwise, the DC voltage is reduced to zero and the
25 negative spikes removed. The audio equalization is switched as appropriate.

1 Alternatively, the ultrasonic measuring system described in relation to **Figure 19** could
2 be employed to detect the active headset.

3
4 Alternatively, a bridge circuit may be used to separate a microphone signal from
5 the headphone drive signal as shown in **Figure 22**. As with the Pulse Width Modulated
6 drive technique describe above, a DC offset is added to the earphone drive signal in
7 order to power the microphone circuitry. The earphone is then capacitor-coupled within
8 the headset to remove the DC offset. The microphone output is converted to a signal
9 dependent current source by op-amp 362, capacitor 364 and resistor 366 and is
10 connected in parallel with the earphone. In the external device, the headset signal is
11 connected into a bridge circuit formed by resistors 368 and 370, impedance 372 and
12 VCA 374. The earphone impedance is modeled by the element Z in the schematic. At
13 the output of the bridge the drive signal that is sent to the earphone is cancelled out to
14 leave just the microphone signal component. For this to work effectively, the
15 microphone signal must be amplified within the headset since it is normally small
16 compared to the earphone signal, hence the need for op-amp 362.

17
18 This technique requires that earphone impedance be known or it will fail due to
19 errors in the subtraction of the drive signal. Earphone resistance may be measured
20 whenever the headset is plugged in to determine the earphone impedance. This may
21 be accomplished by applying a small test signal to the headset, at a level sufficiently
22 low so as to ensure the microphone electronics will not be activated, thus allowing a
23 true measure of the earphone characteristics. The bridge circuit balance is then
24 automatically adjusted by means of the voltage controlled amplifier 374 as shown in
25 **Figure 22**.

1
2 The resistance measurement may be extended to ensure compatibility with a
3 normal headphone by making measurements with a high signal voltage. With a
4 conventional earphone the current drawn will at all times be proportional to the voltage
5 applied, but with the active headset the current will become non-linear as the
6 microphone circuit kicks in, and this can be used for headset identification. The low
7 frequency test signal is outside of the range of hearing so that it does not cause
8 discomfort and is applied before a DC voltage is added. The DC voltage is only added
9 after an active headset is detected. Alternatively, the technique previously discussed of
10 using an ultrasonic signal and a predetermined impedance within the headset may be
11 employed.

12
13 Yet another technique for combining the earphone and microphone connections
14 is to make use of a radio frequency carrier signal as shown in **Figure 23**. As described
15 previously, a DC voltage may be added to the power amplifier output signal and used to
16 power the microphone circuit. In this configuration, the microphone modulates either
17 the amplitude, frequency, or phase of a high frequency oscillator 382, whose output is
18 then capacitively coupled onto the earphone drive by capacitor 384, while being
19 isolated from the microphone by RF inductor 386. The external device power amplifier
20 is fed through an inductor to the output, thus blocking the RF signal from being shorted
21 out by the power amplifier. The RF signal is then passed into demodulator 390 by
22 capacitor 392 and demodulated to recover the microphone signal. A means of
23 detecting a normal headset is used so that the DC level may be removed.

1 Many of the techniques previously described rely upon detecting the presence or
2 absence of an active headset in order to appropriately configure the electronics inside
3 the external device. This identification may be extended to identify the particular model
4 of the active headset in addition to its presence. This would allow the cancellation
5 controller transfer function to be adjusted to suit different active headset arrangements,
6 for instance closed-back versus open-back, without having to measure the transfer
7 function in situ and adapt the electronics accordingly. The headsets may be classified
8 into types in much the same way as cassette tapes, with the type number referring to
9 either performance level or acoustic configuration. This may be taken a step further to
10 prevent the cancellation electronics within the external device from being activated
11 unless a particular brand of headset is used.

12
13 The technique previously described for identifying the headset, whereby the
14 headset is interrogated with an ultrasonic test tone (or multiple test tones) and a
15 particular impedance looked for, or else a coded identification signal is generated within
16 the headset itself may be used to identify the particular model of the active headset in
17 addition to it's presence. This should be straightforward since, whether or not the
18 majority of the electronics is incorporated within the headset itself, power is still needed
19 for the microphone. Thus the headset can house an inexpensive ASIC, connected
20 across the microphone, that injects a low level high frequency digital identification code
21 onto the microphone signal which can be read by corresponding electronics within the
22 external device. The code can either be present all the time or activated for just a short
23 period each time the headset is powered up.

1 The present invention offers several advantages over the prior art. First, all of
2 the noise cancellation circuitry for an active headset may be remote from the headset.
3 Second, some of the noise cancellation circuitry for an active headset may be remote
4 from the headset. Third, a stereo headset is provided with two stereo jack plugs or a
5 six pin connector for connection to the noise cancellation circuitry without requiring
6 replacement of the stereo jack socket by an eight pin socket. It should be noted that
7 the present invention additionally provides for a seven pin connector in the case where
8 it is desirable to use four connections to the headset, as in the prior art, but three
9 connections to the microphone. Such a seven pin connector still uses one less pin than
10 the prior art eight pin connector. Additionally, as previously discussed, three, four and
11 five pin connectors are encompassed by the present invention as well and provide a
12 significant advantage over the prior art eight pin connector. Finally, a transient detector
13 may be provided in the noise cancellation circuitry to overcome noise in the ear due to
14 the generation of transients when plugging in or unplugging the stereo jack plugs of an
15 active headset.

16
17 While the invention has been particularly shown and described with reference to
18 a preferred embodiment, it will be understood by those skilled in the art that various
19 changes in form and detail may be made therein without departing from the spirit and
20 scope of the invention. For instance, it will be understood by one of ordinary skill in the
21 art that the terms headphone and earphone are used interchangeably.